



UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
PIO)
Serial No. **09/779,956**)
Filing Date: **February 9, 2001**)
For: **ELECTRONIC MEMORY CIRCUIT**)
AND RELATED MANUFACTURING)
METHOD)

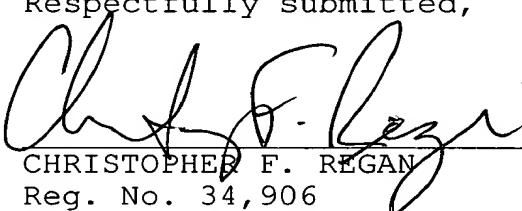
TRANSMITTAL OF FORMAL DRAWINGS

Director, U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Enclosed are twelve (12) sheets of formal drawings
to be filed in the above-identified patent application.

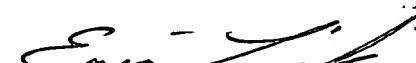
Respectfully submitted,


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